

ADVANCED SEMICONDUCTOR ENGINEERING INC
Form 6-K
February 09, 2006

FORM 6-K
SECURITIES AND EXCHANGE COMMISSION

Washington, D.C. 20549

Report of Foreign Private Issuer

**Pursuant to Rule 13a-16 or 15d-16
of the Securities Exchange Act of 1934**

February 9, 2006

Commission File Number 001-16125

Advanced Semiconductor Engineering, Inc.

(Exact name of Registrant as specified in its charter)

26 Chin Third Road
Nantze Export Processing Zone
Kaoshiung, Taiwan
Republic of China
(Address of principal executive offices)

Indicate by check mark whether the registrant files or will file annual reports under cover of Form 20-F or Form 40-F.

Form 20-F

Form 40-F

Indicate by check mark if the registrant is submitting the Form 6-K in paper as permitted by Regulation S-T Rule 101(b)(1):

Note: Regulation S-T Rule 101(b)(1) only permits the submission in paper of a Form 6-K if submitted solely to provide an attached annual report to security holders.

Indicate by check mark if the registrant is submitting the Form 6-K in paper as permitted by Regulation S-T Rule 101(b)(7):

Note: Regulation S-T Rule 101(b)(7) only permits the submission in paper of a Form 6-K if submitted to furnish a report or other document that the registrant foreign private issuer must furnish and make public under the laws of the jurisdiction in which the registrant is incorporated, domiciled or legally organized (the registrant's home country), or under the rules of the home country exchange on which the registrant's securities are traded, as long as the report or other document is not a press release, is not required to be and has not been distributed to the registrant's security holders, and, if discussing a material event, has already been the subject of a Form 6-K submission or other Commission filing on EDGAR.

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Indicate by check mark whether by furnishing the information contained in this Form, the registrant is also thereby furnishing the information to the Commission pursuant to Rule 12g3-2(b) under the Securities Exchange Act of 1934.

Yes No

If Yes is marked, indicate below the file number assigned to the registrant in connection with Rule 12g3-2(b):

Not applicable

Signatures

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned, thereunto duly authorized.

**ADVANCED SEMICONDUCTOR
ENGINEERING, INC.**

Date: February 9, 2006

By: /s/ Joseph Tung

Name: Joseph Tung
Title: Chief Financial Officer

Item 1

**Advanced Semiconductor Engineering,
Inc.**

FOR IMMEDIATE RELEASE

Contact:

ASE, Inc.

Joseph Tung, CFO / Vice President

Freddie Liu, Financial Controller

Tel: + 886-2-8780-5489

Fax: + 886-2-2757-6121

ir@aseglobal.com

<http://www.aseglobal.com>

**ADVANCED SEMICONDUCTOR ENGINEERING, INC.
ANNOUNCES MONTHLY NET REVENUES**

TAIPEI, TAIWAN, R.O.C., FEBRUARY 8, 2006 □ ADVANCED SEMICONDUCTOR ENGINEERING, INC. (NYSE: ASX, TAIEX: 2311, □ASE□ or the □Company□), announces its unaudited consolidated net revenues for the month of January 2006.

CONSOLIDATED NET REVENUES (UNAUDITED)

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(NT\$ Million)	Jan 2006	Dec 2005	Jan* 2005	Sequential Change	YoY Change
Net Revenues	8,248	9,058	6,268	-8.94%	+31.60%

* January 2005 revenues are retroactively adjusted to net out revenues from discontinuing operations of ASE Malaysia's Camera Module assembly operation.

Safe Harbor Notice:

This press release contains "forward-looking statements" within the meaning of Section 27A of the Securities Act of 1933, as amended, and Section 21E of the Securities Exchange Act of 1934, as amended. Although these forward-looking statements, which may include statements regarding our future results of operations, financial condition or business prospects, are based on our own information and information from other sources we believe to be reliable, you should not place undue reliance on these forward-looking statements, which apply only as of the date of this press release. Our actual results of operations, financial condition or business prospects may differ materially from those expressed or implied in these forward-looking statements for a variety of reasons, including risks associated with cyclical and market conditions in the semiconductor industry; demand for the outsourced semiconductor assembly and testing services we offer and for such outsourced services generally; the highly competitive semiconductor industry; our ability to introduce new testing technologies in order to remain competitive; our ability to maintain a high capacity utilization rate relative to our fixed costs; international business activities; our business strategy; general economic and political conditions; possible disruptions in commercial activities caused by natural or human-induced disasters, including terrorist activity and armed conflict; our future expansion plans and capital expenditures; fluctuations in foreign currency exchange rates; and other factors. For a discussion of these risks and other factors, please see the documents we file from time to time with the Securities and Exchange Commission, including our 2004 Annual Report on Form 20-F filed on June 23, 2005.